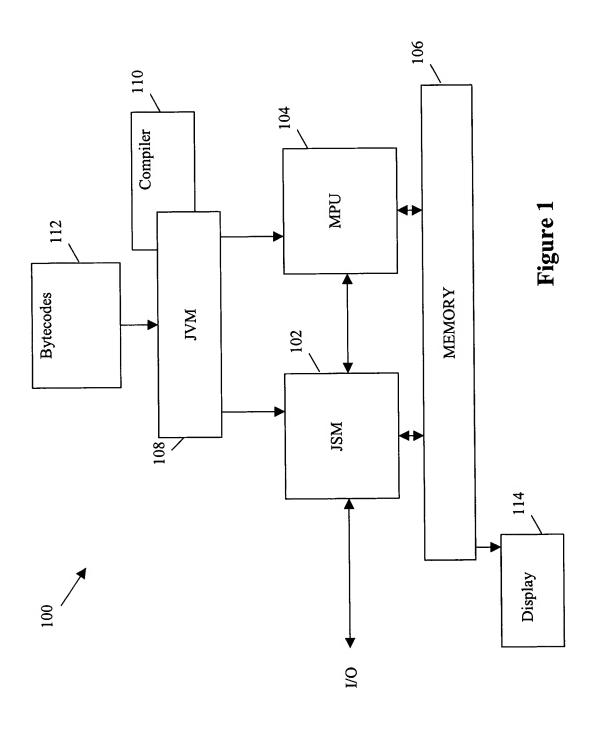
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Sheet 2 of 5 MPU I-Cache Pre-Decode Logic Instruction Fetch Logic 158 μ-TLB Decode Logic 156 I-RAM 154 152 132 130 μ-Stack Figure 2 ALU D-RAM 148 126 AGU 147 124 162 160 140 D-Cache Program Counter Status Register AGU Registers 144 122 142 120

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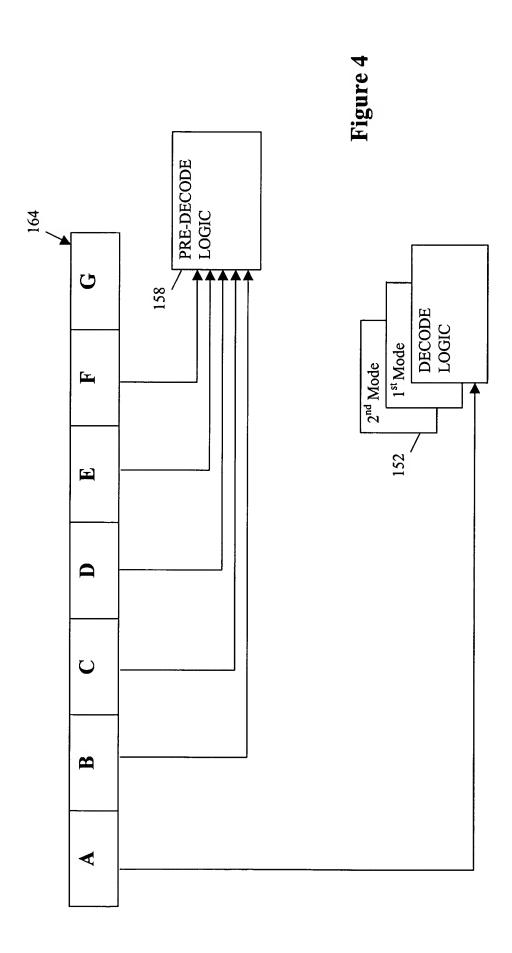
Figure 3 General Purpose/Indirect Register Index (IRI) General Purpose/Local Variable Pointer (LV) General Purpose/Address Index 1 (AI1) General Purpose/Address Index 0 (AI0) Micro-program counter (micro-PC) Status and Control (ST) Program Counter (PC) General Purpose (GP) Top of Stack (ToS) Stack Pointer (SP) R10 R12 R13 R14 R15 **R**11 \mathbb{R}^0 **R4 R6 R8** R1 \mathbb{Z} \mathbb{R}_3 **R5 R7 R**9 160 162

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